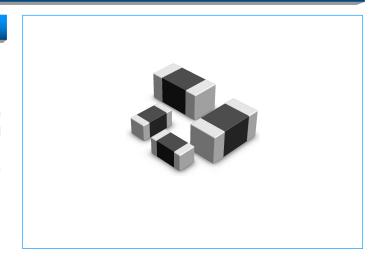




SV0402N180G1B

Description

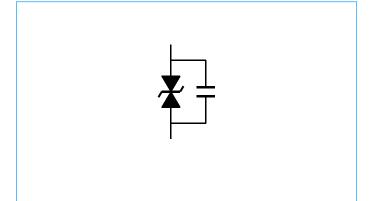
The SV0402N180G1B is based on Multilayer fabrication technology. These components are designed to suppress a variety of transient events, including those specified in IEC 61000-4-2 or other standards used for Electromagnetic Compliance (EMC). The SV0402N180G1B is typically applied to protect integrated circuits and other components at the circuit board level. It can operate over a wider temperature range than zener diodes.



Features

- SMD type zinc oxide based ceramic chip
- Lead free plating termination provided good solderability characteristic
- Insulator overcoat keeps excellent low and stable leakage current
- Quick response time (<1ns)
- Low clamping voltage
- High transient current capability
- ◆ Compact size for EIA 0402

Equivalent Circuits



Applications

- Application for Mother Board, Notebook, Cellular Phone, PDA, handheld device, DSC, DV, Scanner, and Set-Top Box...etc.
- Suitable for Push-Button, Power Line and Low Frequency single line over-voltage protect.





SV0402N180G1B

Electrical Characteristics (25±5℃)

Symbol	Minimum	ТурісаІ	Maximum	Units
V _{RMS}	_	_	14	V
V _{DC}	_	_	18	V
V _V	23	_	33	V
Vc	_	_	54	V
C _P	_	85	_	pF
I _{max}	_	_	20	А
W _{max}	_	_	0.05	W

 V_{RMS} - Maximum AC operating voltage the varistor can maintain and not exceed 10 μ A leakage current.

V_{DC} - Maximum DC operating voltage the varistor can maintain and not exceed 10μA leakage current.

 $\mathbf{V_V}$ - Voltage across the device measure at 1mA DC current. Equivalent to V_B "breakdown voltage".

V_c - Maximum peak current across the varistor with 8/20µs waveform and 1A pulse current.

C_P - Device capacitance measured with zero volt bias 1Vrms at 1MHz.

I_{max} - Maximum peak current which may be applied with 8/20μs waveform without device failure.

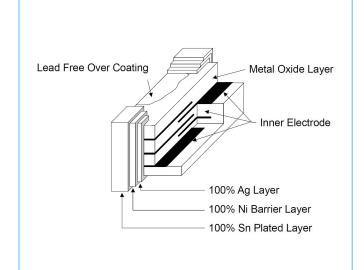
 W_{max} - Maximum energy which may be dissipated with the 10/1000 μ s waveform without device failure.

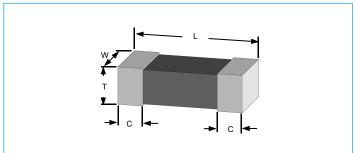




SV0402N180G1B

Construction & Dimensions





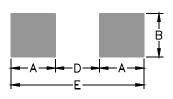
Size EIA (EIAJ)	0402 (1005)				
Symbol	Inches	Inches Millimeters			
L	0.038±0.005	0.96±0.12			
w	0.019±0.003 0.48±0.0.07				
Т	0.020±0.004	0.50±0.10			
С	0.010±0.006 0.25±0.15				

Pad Layouts & Precaution for handling of substrate

Solder cream in reflow soldering

Refer to the recommendable land pattern as printing mask pattern for solder cream.

(1) Print solder in a thickness of 150 to 200 μm



Size EIA (EIAJ)	0402 (1005)			
Symbol	Inches	Millimeters		
Α	0.024	0.60		
В	0.020	0.51		
D	0.020	0.50		
E	0.067	1.70		

Precaution for handling of substrate

Do not exceed to bend the board after soldering thes product extremely. (reference examples)

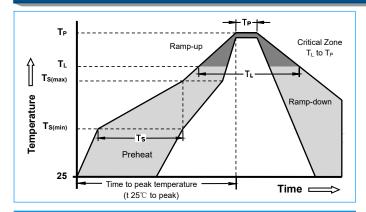
- Mounting place must be as far as possible from the position, which is close to the break line of board or on the line of large holes of board.
- Do not bend extremely the board, in mounting another component. If necessary, use back-up pin (support pin) to prevent from bending extremely.
- Do not break the board by hand. We recommend to use the machine or the jig to break it.





SV0402N180G1B

Soldering Parameters



Precaution for Soldering

Note that this product will be easily damaged by rapid heating, rapid cooling or local heating.

Do not give heat shock over 100°C in the process of soldering. We recommend to take preheating and gradual cooling

Soldering gun procedure

Note the follows, in case of using solder gun for replacement.

- 1) The tip temperature must be less than 280 for the period within 3 seconds by using soldering gun under 30W
- 2) The soldering gun tip shall not touch this product directly.

Soldering volume

Note that excess of soldering volume will easily get crack the body of this product.

Reflow Co	ndition	Pb-Free assembly		
	-Temperature Min (T _{s(min)})	+150°C		
Pre Heat	-Temperature Max (T _{s(max)})	+200°C		
	-Time (min to max) (T _s)	60 -180 Seconds		
Average ra	amp up rate (Liquidus Temp T _L)	3°C/Second Max		
T _{S(max)} to T	∟ - Ramp-up Rate	3°C/Second Max		
Reflow	- Temperature (T _L) (Liquidus)	+217°C		
Kellow	- Time (min to max) (T _L)	60 -150 Seconds		
Peak Temp	perature (T _P)	260 +0/-5°C		
Time withi Temperatu	n 5°C of actual peak ıre (T _P)	20-40 Seconds		
Ramp-dow	n Rate	6°C/Second Max		
Time 25°C	to peak Temperature (T _P)	8 minutes Max		

General Technical Data

Operating Temperat	ture	-40 ~ +85°C		
Storage Temperature		-40 ~ +85°C		
Response Time		<1 ns		
Solderability		245±5°C, 3±1sec		
Solder leach resista	nce	260±5°C, 10±1sec		
T	Storage Temperature	5 ~ 40°C		
Taping Package Storage Condition	Relative Humidity	To 65%		
	Storage Time	12 Months max		

Environmental Performance

Item	Specifications	Test Condition		
Bias Humidity $\triangle V_V / V_V \le \pm 10 \%$		90%RH, 40°C, Working Voltage, 1000 hrs		
Thermal Shock	△V _V / V _V ≤ ±10 %	-40°C to 85°C, 30 min. cycle, 5 cycles		
Full Load Voltage	$\triangle V_V / V_V \le \pm 10 \%$	Working Voltage, 85°C,1000 hrs		

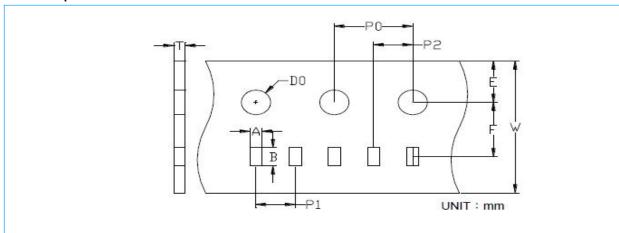




SV0402N180G1B

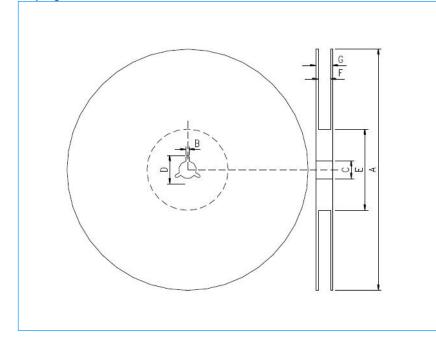
Taping Package and Label Marking

Carrier Tape Dimensions



Size EIA (EIAJ)	0402 (1005)									
Symbol	A ±0.03									
Unit: mm	0.59	1.12	8.0	1.75	3.5	4.0	2.0	2.0	1.55	0.60

Taping Reel Dimensions Unit: mm



Taping Specifications

There Shall be the portion having no product in both the head and the end of taping, and there shall be the cover tape in the heat of taping.





SV0402N180G1B

Taping Reel Dimensions (Continue)

Size EIA (EIAJ)	0402 (1005)							
Symbol	A ±2.0							
Unit: mm	178.0	2.0	13.0	21.0	62.0	9.0	13.0	

Quantity of products in the taping package

SIZE EIA	0402
(EIAJ)	(1005)
Standard Packing Quantity (PCS / reel)	10,000