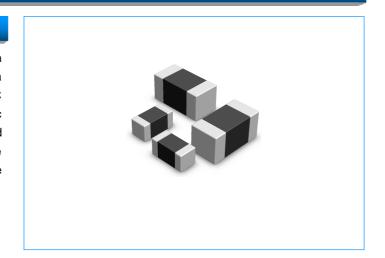




## SV0402E5R5G4B

#### **Description**

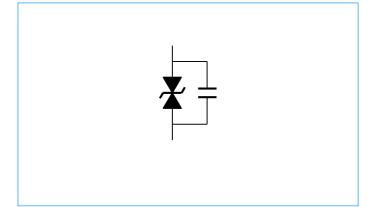
The SV0402E5R5G4B is based on Multilayer fabrication technology. These components are designed to suppress a variety of transient events, including those specified in IEC 61000-4-2 or other standards used for Electromagnetic Compliance (EMC). The SV0402E5R5G4B is typically applied to protect integrated circuits and other components at the circuit board level. It can operate over a wider temperature range than zener diodes.



#### **Features**

- u Lead Free type
- u SMD type zinc oxide based ceramic chip
- Insulator over coat keeps excellent low and stable leakage current
- Plating termination provided good solderability characteristic
- Quick response time (<1ns)</li>
- u Low clamping voltage
- u Meet IEC61000-4-2 standard
- Low capacitance can meet high speed single transient voltage protection

### **Equivalent Circuits**



#### **Applications**

- Low capacitance product applications for high-speed signal lines such as HDMI, DVI, USB, IEEE 1394 Port etc.
- U Normal capacitance product applications for I/O Port (RS232, USB, PS2, VGA, Audio) on Mother Board and Notebook, Set–Top Box, MP3 Players, DVD Players, and Docking System etc.





## SV0402E5R5G4B

## **Electrical Characteristics (25±5℃)**

Symbol	Minimum	Typical	Maximum	Units
V <sub>DC</sub>	_	_	5.5	V
Vv	9	_	19	V
Vc	_	_	40	V
СР	_	9	_	pF

V<sub>DC</sub> – Maximum DC operating voltage the varistor can maintain and not exceed 10μA leakage current.

V<sub>V</sub> – Voltage across the device measure at 1mA DC current.

Equivalent to V<sub>B</sub> "breakdown voltage"

V<sub>c</sub> - Maximum peak current across the varistor with 8/20µs waveform and 1A pulse current.

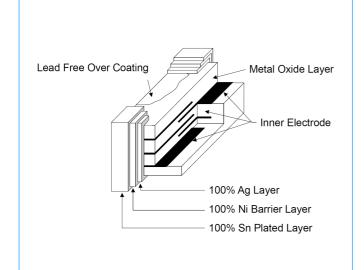
 $C_P$  — Device capacitance measured with zero volt bias 1Vrms at 1MHz. The pF is  $\pm 30\%$ .

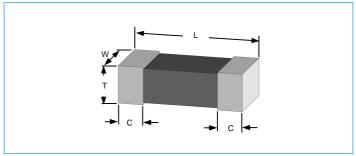




## SV0402E5R5G4B

#### **Construction & Dimensions**





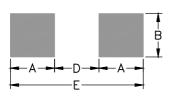
Size EIA (EIAJ)	0402 (1005)	
Symbol	Inches Millimeters	
L	0.038±0.005	0.96±0.12
w	0.019±0.003	0.48±0.0.07
Т	0.020±0.004	0.50±0.10
С	0.010±0.006	0.25±0.15

## Pad Layouts & Precaution for handling of substrate

#### Solder cream in reflow soldering

Refer to the recommendable land pattern as printing mask pattern for solder cream.

(1) Print solder in a thickness of 150 to 200 µm



Size EIA (EIAJ)	0402 (1005)	
Symbol	Inches Millimeters	
Α	0.024	0.61
В	0.020	0.51
D	0.020	0.51
E	0.067	1.70

#### Precaution for handling of substrate

Do not exceed to bend the board after soldering thes product extremely. (reference examples)

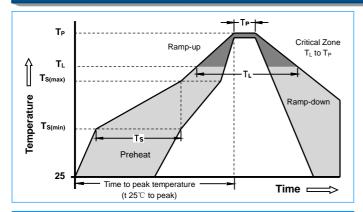
- I Mounting place must be as far as possible from the position, which is close to the break line of board or on the line of large holes of board.
- I Do not bend extremely the board, in mounting another component. If necessary, use back-up pin (support pin) to prevent from bending extremely.
- I Do not break the board by hand. We recommend to use the machine or the jig to break it.





## SV0402E5R5G4B

## **Soldering Parameters**



## **Precaution for Soldering**

Note that this product will be easily damaged by rapid heating, rapid cooling or local heating.

Do not give heat shock over 100°C in the process of soldering. We recommend to take preheating and gradual cooling

#### Soldering gun procedure

Note the follows, in case of using solder gun for replacement.

- 1) The tip temperature must be less than 280 for the period within 3 seconds by using soldering gun under 30W
- 2) The soldering gun tip shall not touch this product directly.

#### Soldering volume

Note that excess of soldering volume will easily get crack the body of this product.

Reflow Condition Pb-Free assemb		Pb-Free assembly
Pre Heat	-Temperature Min (T <sub>s(min)</sub> )	+150°C
	-Temperature Max (T <sub>s(max)</sub> )	+200°C
	-Time (min to max) (T <sub>s</sub> )	60 -180 Seconds
Average ramp up rate ( Liquidus Temp T <sub>L</sub> ) to peak		3°C/Second Max
T <sub>S(max)</sub> to T <sub>L</sub> - Ramp-up Rate		3°C/Second Max
Reflow	- Temperature (T <sub>L</sub> ) (Liquidus)	+217°C
	- Time (min to max) (T <sub>L</sub> )	60 -150 Seconds
Peak Temp	Peak Temperature (T <sub>P</sub> ) 260 +0/-5°C	
Time within 5°C of actual peak Temperature (T <sub>P</sub> )		20-40 Seconds
Ramp-down Rate		6°C/Second Max
Time 25°C to peak Temperature (T <sub>P</sub> )		8 minutes Max

## **General Technical Data**

Operating Temperature		-40 ~ +85°C
Storage Temperature		-40 ~ +85°C
Response Time		<1 ns
Solderability		245±5°C, 3±1sec
Solder leach resistance		260±5°C, 10±1sec
Taping Package Storage Condition	Storage Temperature	5 ~ 40°C
	Relative Humidity	To 65%
	Storage Time	12 Months max

#### **Environmental Performance**

Item	Specifications	Test Condition
Bias Humidity	$\triangle V_V / V_V \le \pm 10 \%$	90%RH, 40°C, Working Voltage, 1000 hrs
Thermal Shock	△V <sub>V</sub> / V <sub>V</sub> ≤ ±10 %	-40°C to 85°C, 30 min. cycle, 5 cycles
Full Load Voltage	$\triangle V_V / V_V \le \pm 10 \%$	Working Voltage, 85°C,1000 hrs

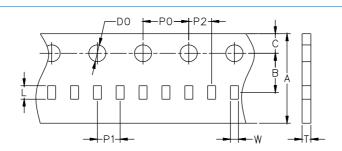




## SV0402E5R5G4B

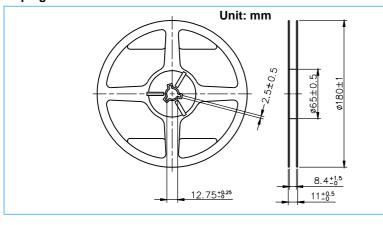
## **Packaging Information**

#### **Carrier Tape Dimensions**



Size EIA (EIAJ)	0402 (1005)	
Symbol	Inches	Millimeters
Α	0.315±0.012	8.00±0.30
В	0.138±0.002	3.50±0.05
С	0.069±0.002	1.75±0.05
D0	0.061±0.002	1.55±0.05
P0	0.157±0.004	4.00±0.10
P1	0.079±0.002	2.00±0.05
P2	0.079±0.002	2.00±0.05
W	0.023±0.001	0.59±0.03
L	0.044±0.001	1.12±0.03
Т	0.024±0.001	0.60±0.03

#### **Taping Reel Dimensions**



### **Taping Specifications**

There Shall be the portion having no product in both the head and the end of taping, and there shall be the cover tape in the heat of taping.

## Quantity of products in the taping package

SIZE EIA	0402
(EIAJ)	(1005)
Standard Packing Quantity (PCS / reel)	10,000

#### The contents of a box:

0402 Series: 6 reels / inner box